

**AMENDMENTS TO THE CLAIMS**

1. (Previously Presented) A field sequential liquid crystal display device, comprising:
  - a liquid crystal panel having an upper substrate, a lower substrate and a liquid crystal layer therebetween;
  - a backlight device under the liquid crystal panel for irradiating light to the liquid crystal panel and having three color light sources; and
  - an image signal processor controlling a lighting order and combination of the three color light sources,
  - wherein said image processor controls said combination and lighting order in relation to a timing frame,
  - wherein said frame is divided into subframes,
  - wherein said image signal processor decides the combination of the three color light sources at each subframe,
  - wherein the three color lights are selectively lit at each subframe, and
  - wherein at least two of the color light sources are lit in at least one subframe.
2. (Original) The device according to claim 1, wherein each of the three color light sources has one of colors Cyan, Magenta and Yellow.
3. (Original) The device according to claim 1, wherein each of the three color light sources has one of colors Red, Green and Blue.
4. (Original) The device according to claim 1, wherein the image signal processor changes the lighting order and combination of the three color light sources depending on image characteristics displayed in the liquid crystal panel.
5. (Original) The device according to claim 1, wherein the liquid crystal layer is Optical Compensated Birefringent (OCB) mode.

6. (Original) The device according to claim 1, wherein the liquid crystal layer is Ferroelectric Liquid Crystal (FLC) mode.

7. (Previously Presented) The device according to claim 1, wherein the three color light sources are sequentially lit for up to about  $1/180^{\text{th}}$  of a second during three subframes, and wherein one frame period is approximately  $1/60^{\text{th}}$  of a second.

8. (Original) The device according to claim 7, wherein a lighting time of each of the light sources at each subframe is less than  $1/180$  second.

9-22. (Canceled)